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Application No.	101041,753	Prepared by	S.Winslow	
Examiner-GAU	Thompson - 2825	Date	10-29-04	
		No. of queries	1	
			REVERSE	

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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	MESSAGE	
	PTO-1449 (4 FORMS): Please either initial or line through citations.	
CLAIMS		
initials <i>SW</i>		
RESPONSE		
initials		

Substitute (Modified)	Form PTO-1449	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-608001	Application No. 10/041,753
Information Disclosure Statement by Applicant <small>(Use several sheets if necessary)</small> <small>(37 CFR §1.98(b))</small>		Applicant Timothy J. Fennell et al.		
		Filing Date January 7, 2002	Group Art Unit 2825	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	RE 38,059	04/2003	Yano, et al.			
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Examiner Signature	Date Considered
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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Substitute Disclosure Form (PTO-1449)

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	AYYY	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345.
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	BBBBB	Su, Stephen, "An Interactive Design Automation System", <i>Proceedings of the 10th Design Automation Workshop on Design Automation</i> , pp. 253-261, June 1973

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